

## “Test Result Correlation” versus “Diagnostic Reasoning”

Integrating advanced on-board Health Management systems and off-board Diagnostic

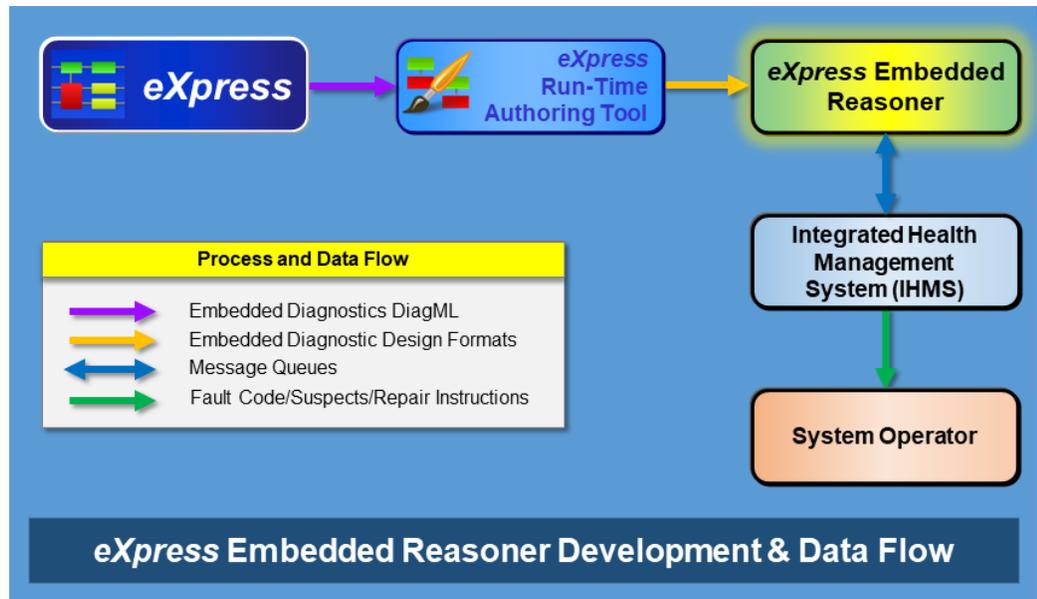
As of now, DSI has succeeded in bringing its new “eXpress Embedded Reasoner” to industry! This is a capability that Industry has waited many years to see finally materialize!

**Q: Didn't the industry already have “Diagnostic Reasoning” for complex assets where it would conduct “Health Status” checks by using Sensors and on-board BIT?**

**A: Actually, not really, and there is a significant difference.**

As system functional status checks are performed by the on-board BIT that may use a variety of advanced sensing technologies, the data received from the sensors enable the determining of the system's health status. The data used to determine the health status of the system is reported in terms of whether or not the executed functional BIT test either “passes” or “fails”.

As multiple sensors are used to report health status of any number of functions on a continuous basis, the collection of the aggregate reporting of the on-board BIT simultaneously is able to establish a Fault Signature. The fault signature is typically “correlated” against any pre-defined set(s) of possible “passed” or “failed” BIT that have previously been determined to represent any failure(s) that may exist, or likely to manifest into a failure within the system.



On complex systems, this “BIT Status” reporting is imperative and provides data to the Health Management (“HM”) systems for executing near instantaneous operational mitigating or corrective actions.

While this is typical and essential for most on-board HM applications, the corrective or mitigating actions must error on the side of being “aggressively conservative”. For example, a corrective action may be to switch to a redundant path or degraded mode when a BIT Fault Signature “correlates to” any pre-defined BIT Failure Code Signatures – or any derivative “seemingly” thereof.

In the experiencing of such critical events, there is no value in expending any time to compute any root cause of the failure(s). Instead, most every mitigating corrective action for HM systems will appropriately prioritize safety, and the asset will immediately pursue that objective.

### Inside this Issue...

Test Correlation vs. Diagnostic Reasoning	1-3
eXpress Maintenance Module	4
DSI Training Schedule	4

### Did You Know?

... That with the eXpress diagnostic engineering tool that all BIT codes can “carry” full diagnostic conclusions to the off-board environment – far more comprehensive than any other method or technology? It is simple and just another seamless capability! Ask DSI today!

Visit the DSI Website at [www.dsiintl.com](http://www.dsiintl.com)!

Diagnostic ambiguity is acceptable when traded against the pursuit of time-critical safety – even if an operation or mission was aborted falsely!

However, once the urgency has been mitigated, the asset will need to be “diagnosed” before being placed back into service. Here is where the “Diagnostic Reasoning” is King.

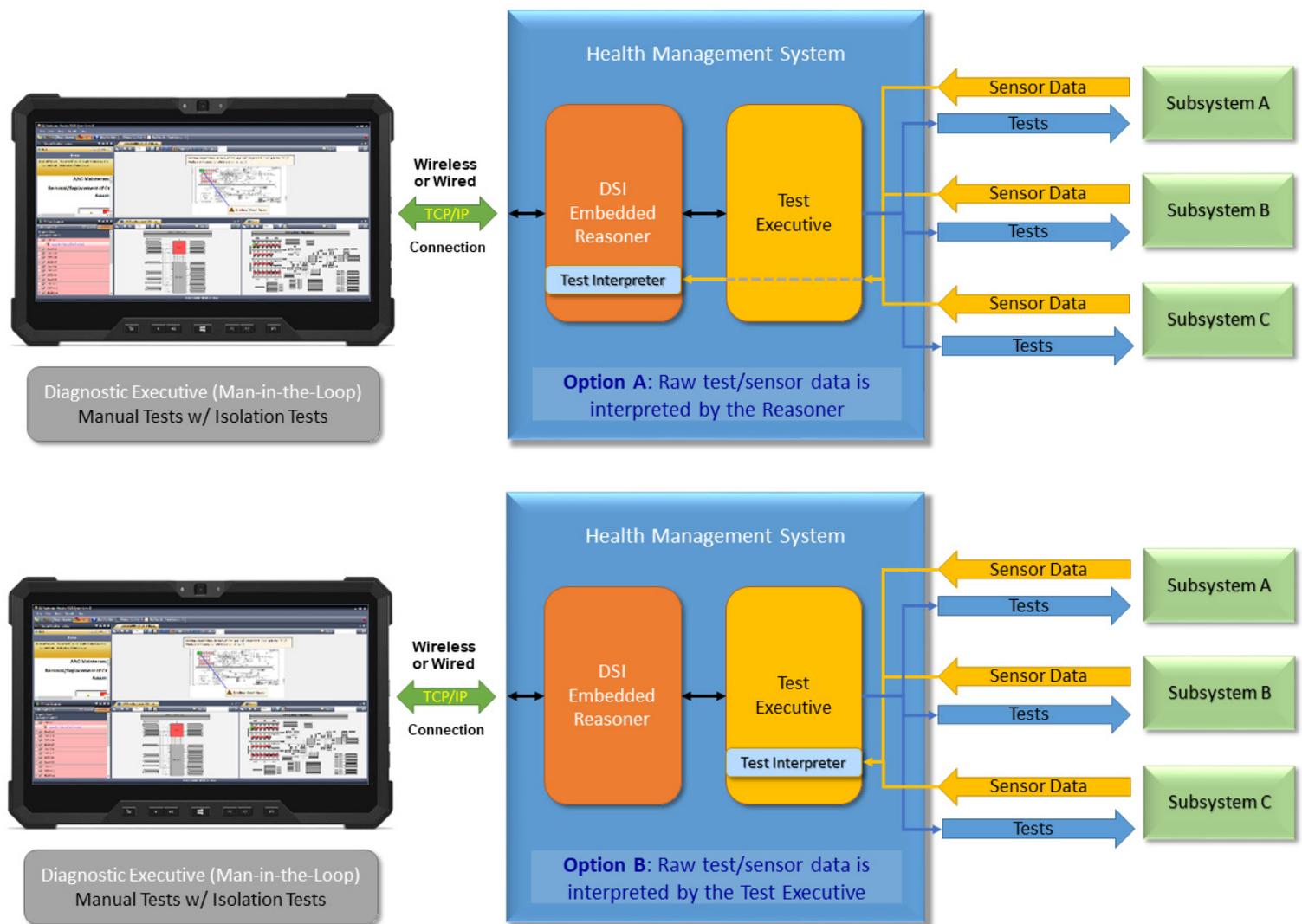
**Traditional Health Management Systems:  
Strong on-board – Weak in seeding off-board Diagnostics**

Unfortunately, on-board HM systems do not typically provide as much insight to off-board diagnostics despite what most everyone gives them credit for. In fact, they are quite stingy. All that the on-board HM system is typically able to provide is simple entry point into the “off-board” or next “diagnostic level” using the BIT “Test Results”. That is all that is carried to the diagnostic environment. Unfortunately, traditional on-board HM system designs essentially dismiss the opportunity to provide sufficient Diagnostic detail that could largely benefit the purposes of a cost effective sustainment paradigm. This is sinful for anyone aspiring to be a “Whole-Life” or “Life Cycle” Systems engineering juggernaut.

On-board HM Systems target very specific purposes. In fact, the traditional requirements for the on-board HM is blissfully unaware of the tremendous advantage that could be gained from becoming more concerned about gathering and “bridging” of the on-board “Test Result Correlation” to the off-board Diagnostic application. It’s rather simple, and a sustainment “cash cow”, when the customer or asset owner is in-the-know.

*How does the eXpress Embedded Reasoning differ?*

The **eXpress** Embedded Reasoner is not intended to serve as an immediate, risk-mitigating capability as is the on-board HM systems described above. Instead, the **eXpress** Embedded Reasoning is able to function contemporaneously with this on-board HM, if any. If not, the Embedded Reasoner is still able to uniquely gather extensive diagnostic detail from the on-board BIT status, enabling it to form and transfer “diagnostic conclusions” to the off-board diagnostic or maintenance environment.



**Other Options and Configurations are Available and can be Customized to Each Customer's Needs**

The two illustrations above for Health Management Configurations depict how the DSI eXpress Embedded Reasoner can be fielded in an operational / maintenance system using ruggedized tablets or laptops running DSI Workbench.

**Encapsulation of “Diagnostic Conclusions” within Fault Codes**

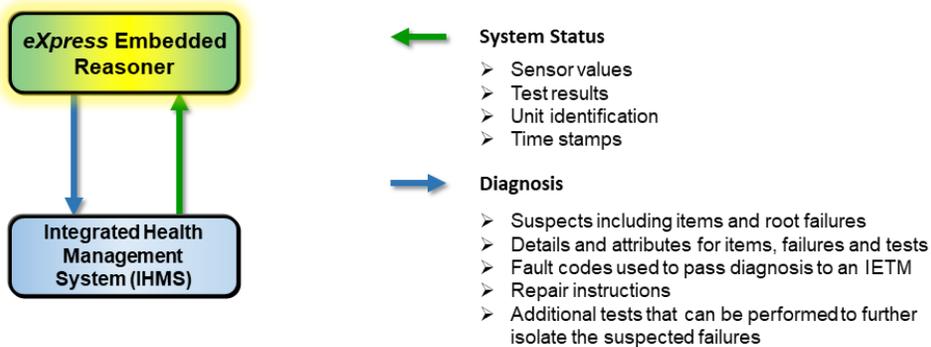
These diagnostic conclusions are encapsulated within the Fault Codes that have been pre-defined within the advanced **eXpress** diagnostic engineering activity. As a result, the Fault Codes will contain extensive diagnostic inferencing capabilities that are not otherwise possible. As the design is modified or is changed over the sustainment life cycle, this technology within **eXpress** allows for the immediate reconfiguring of the Fault Codes to contain any modified diagnostic detail with minimal human interactivity and virtually no opportunity for error.

**How do we take advantage of this diagnostic savvy?**

Since the diagnostic savvy is contained on-board within the **eXpress** Embedded Reasoner, the diagnostic detail can be more comprehensive if **eXpress** is used to influence the design during design development.

However, any possible diagnostic detail that can be extracted from any existing legacy design is also able to fully contained within the **eXpress** diagnostic engineering paradigm. In such case, limitations to gaining full diagnostic detail is determined by the inherent diagnostic capability of the legacy design.

Still, vast new diagnostic inferencing is able to be gained from system and health status interrogation by the on-board BIT. However, this is NOT a capability of any asset deployed today where the diagnostic design is not captured by **eXpress**.



**Messaging between the embedded reasoner and the IHMS**

**How do we realize the maximum potential of the off-board effectiveness gained from our existing HM or while using the eXpress Embedded Reasoner?**

From this second-level diagnostic entry point, all available diagnostic detail gained from the on-board HM system is transferred to the off-board (second level) diagnostic paradigm. If a particular IETM is required in the addressing of IETM requirements, then the diagnostic capability is able to be transferred to that environment.

For a most diagnostically-effective experience that can be used equally by most any skill level technician, DSI Workbench is able to be hosted on any approved portable and capable device to realize the maximum “potential” off-board diagnostic effectiveness

**Diagnostic Conclusions are fully transferable**

- Choose the environment or constraints – whether it shall be the field, shop, ship, depot, production lab, on vehicle console.
- Transfer the diagnostic knowledge – for use to any sustainment paradigm or solution. Although the images (below, etc.) depict the use of the full DSI Workbench capability in a generic Portable Maintenance application, the diagnostic acumen can be integrated with most any simple or high-end Test Solution.
- Choose your device - whether it be DSI Workbench directly, embedded within or via an API with high-end or specialized ATE for any highly capable solution(s) including NGATS, OPATS or any Joint ATS solution that may need to be compatible with ATML, S1000-D data formats or specific to any unique implementation.



- Reduce or Eliminate CND’s, NFF’s, RTOK’s – typical symptoms of weak diagnostic engineering. One of the most avoidable contributors to the runaway sustainment costs compelling the living with evolving “Can Not Duplicates”, “No Fault Found”, or “Re-Test OK’s”. These largely occur due to the loss of diagnostic certainty between the operational system and the maintenance activity.
- Track or record resolution history – view images or videos of test or repair solutions, that focus right to the lowest repairable/replaceable item(s) or root cause every time. Never misdiagnose again!

## eXpress Maintenance Module

The **eXpress** Maintenance Module allows you to extend diagnostics created within **eXpress** to address practical concerns that arise when integrating diagnostics into an overall maintenance solution:

- **Encapsulation and Preservation of Expert Design Knowledge** within Diagnostic Procedures at Different Maintenance Levels, or between Diagnostics and Maintenance Procedures / Electronic Technical Manuals.
- **Development of Customizable Maintenance Procedures**, Optimized in Accordance with Logistics Goals (Sustainment Cost, Availability, Spare Parts Stock Levels, Logistics Footprint).

The **eXpress** Maintenance Module provides a set of features that allow you to extend diagnostics created within **eXpress** to address a variety of practical situations:

- **Coordinated Development** of Diagnostic & Maintenance Procedures
- **Multi-Purposing of Data** within Different Diagnostic & Maintenance Engineering Efforts
- **Concurrent Engineering** of Diagnostic & Maintenance Tasks
- **Realistic Assessments** of the Diagnostic & Maintenance Procedures that are to be deployed

Visit the DSI website to Learn More!

[www.dsiintl.com](http://www.dsiintl.com)

**eXpress Maintenance Module Features**

Fault Codes Report			
##	Fault Code	## Items	Mapped Element(s)
1	0000000	6	ECU::DIREG F Pump PS Pump Relay R Pump PS ECU::RISC Processor ECU::Solenoid Controller CMOS Switch
2	0000001	2	LF Disc Assy::ABS Speed Sensor LF Disc Assy::Tone Ring
4	0000002	2	RF Disc Assy::ABS Speed Sensor RF Disc Assy::Tone Ring
5	0000003	2	ECU::DIREG SENS CONN 2A
8	CAL0200	1	RF Disc Assy::Caliper Assy
9	CAL0201	1	RR Disc Assy::Caliper Assy

## Training Course Schedule

Course Number	Pre-requisite	Course Description	Dates	Location	POC
T-100		System Diagnostics Concepts and Applications	Feb 12, 2018	Orange, CA	info@dsiintl.com
T-110	T-100	Basic Modeling & Introduction to Testing	Feb 12-14, 2018	Orange, CA	info@dsiintl.com
T-120	T-110	Introduction to Testing & Analysis	Feb 14-16, 2018	Orange, CA	info@dsiintl.com
<b>ADVANCED TRAINING COURSES</b>					
T-200	T-120	Advanced Model Development and Analysis	Mar 5-6, 2018	Orange, CA	info@dsiintl.com
T-205	T-200	Advanced Test Development and Importing	Mar 7-9, 2018	Orange, CA	info@dsiintl.com
T-240	T-205	FMECA and FTA Development and Assessment	Mar 26-27, 2018	Orange, CA	info@dsiintl.com
T-250	T-205	STAGE Time-Based Assessments and Principles	Mar 28, 2018	Orange, CA	info@dsiintl.com
T-260	T-205	RTAT and DSI Workbench Theory and Application	Apr 10-11, 2018	Orange, CA	info@dsiintl.com



## World Wide Representatives



United States	United Kingdom	France	Japan	South Korea	China
DSI International, Inc. (714) 637-9325 info@dsiintl.com <a href="http://www.dsiintl.com">www.dsiintl.com</a>	Sphera Test Engineering Services +44 (0)1202 868585 chris.gorringe@sphera.co.uk <a href="http://www.sphera.com">www.sphera.com</a>	Sphera Test & Services +33 (0)6 88 61 33 91 michel.schieber@sphera.com <a href="http://www.sphera.com">www.sphera.com</a>	Y-MAX, Inc. +81-45-789-0701 tets@y-max.com.jp <a href="http://www.y-max.com">www.y-max.com</a>	Realtimewave Co. Ltd +82-2-572-9471/2 sales@realtimewave.com <a href="http://www.realtimewave.com">www.realtimewave.com</a>	MTCS Systems Engineering Co. Ltd +86-10-5881-6565 sales@mtcs.com.cn <a href="http://www.mtcs.com.cn">www.mtcs.com.cn</a>

The eXpress™ Newsletter is published semi-annually by DSI International

All information in this publication is used and covered by copyright and/or Trademark of DSI and/or respective manufacturers. Readers are encouraged to reproduce articles, giving credit to DSI. Additional copies may be obtained by contacting DSI at the above email address.